

FIG. 1

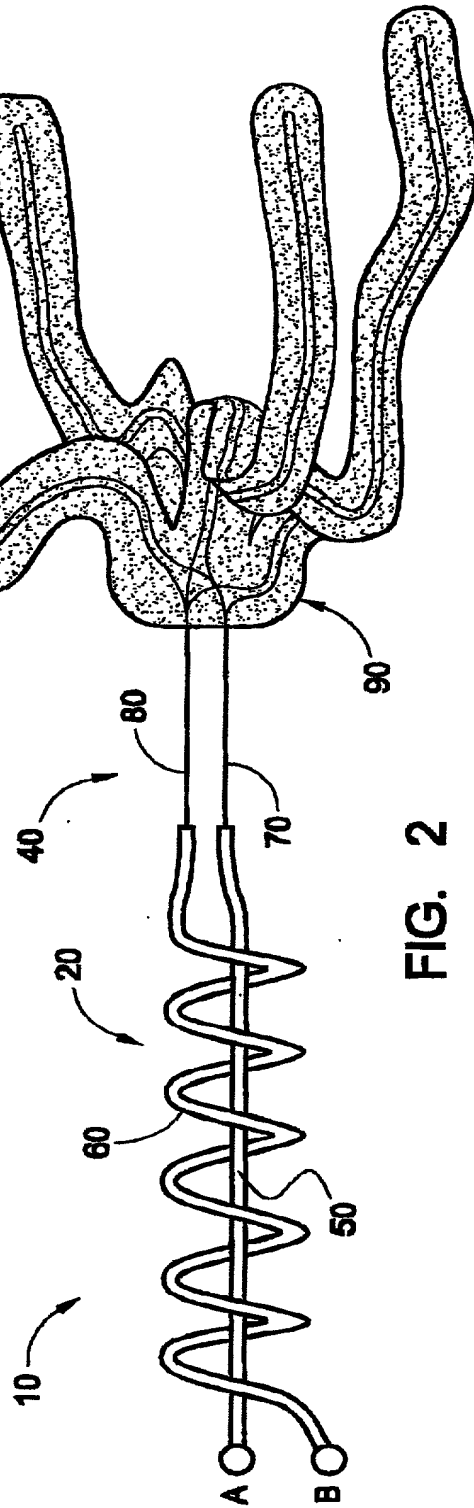


FIG. 2

FIG. 3 is a schematic diagram of a circuit for testing a device. The circuit includes a power source 150, a switch 140, and two terminals A and B. The device 10 is connected between terminals A and B. The circuit is shown in two states: a first state where the switch 140 is open and a second state where the switch 140 is closed.

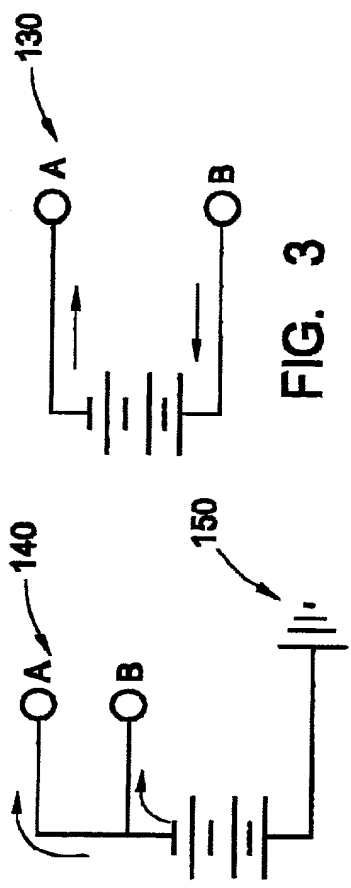
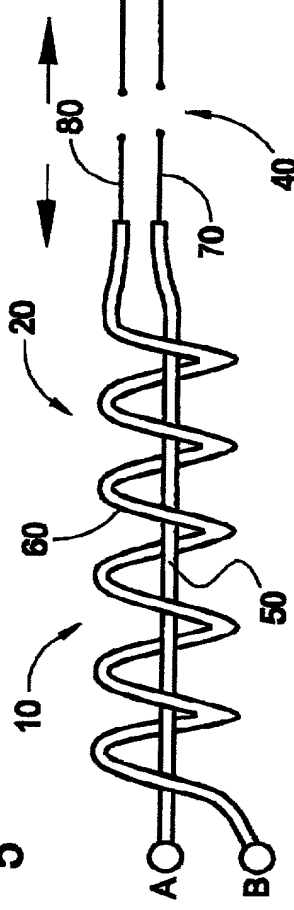


FIG. 3

FIG. 5



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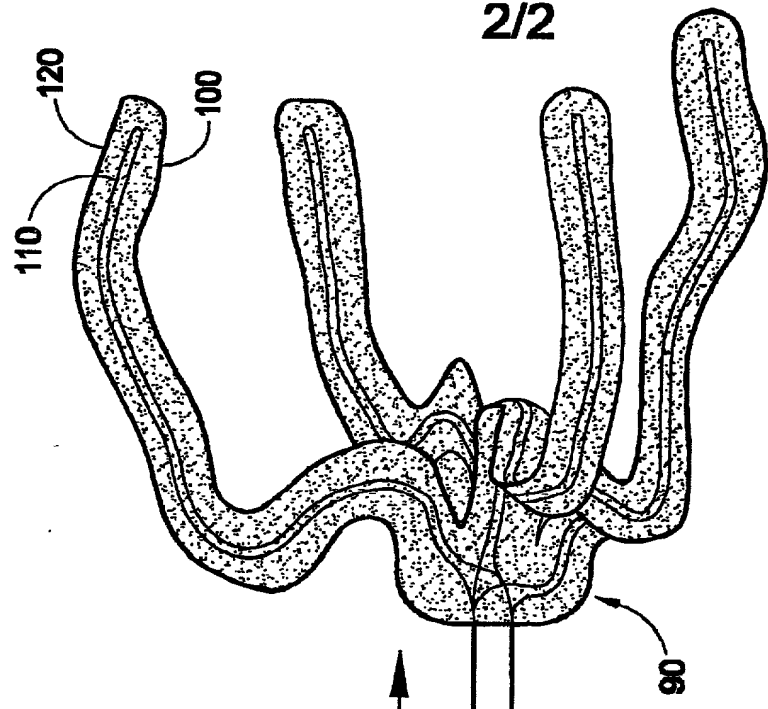


FIG. 4

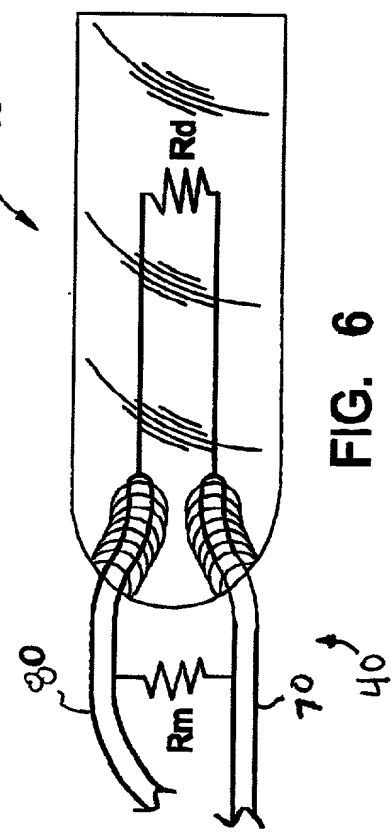


FIG. 6

